

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2705	multiplexer and (pass-gate or (pass adj1 gate) or (transmission-gate) or (transmission adj1 gate))	US-PGPUB; USPAT	OR	OFF	2005/08/09 10:51
L2	1912	1 and ((memory adj1 cell) or latch\$2) and (inver\$5 or complement\$5)	US-PGPUB; USPAT	OR	OFF	2005/08/09 11:20
L3	454	2 and (pld or fpga or (programmable adj1 logic))	US-PGPUB; USPAT	OR	OFF	2005/08/09 11:17
L4	276	3 and ((inver\$5 NEAR1 input\$1) or (complementary NEAR1 input\$1))	US-PGPUB; USPAT	OR	OFF	2005/08/09 11:49
L5	64	4 and ((second adj1 multiplex\$2) or (third adj1 multiplex\$3))	US-PGPUB; USPAT	OR	OFF	2005/08/09 11:50

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	((first adj1 multiplex\$2) and (second adj1 multiplex\$2) and ((memory adj1 cell) or latch\$2) and ((complementary NEAR2 input\$1) or (inver\$4 NEAR2 input\$1))).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/09 12:02
L2	0	((plurality adj2 multiplex\$2) or (multiple adj1 multiplex\$2) and ((memory adj1 cell) or latch\$2) and ((complementary NEAR2 input\$1) or (inver\$4 NEAR2 input\$1))).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/09 12:04